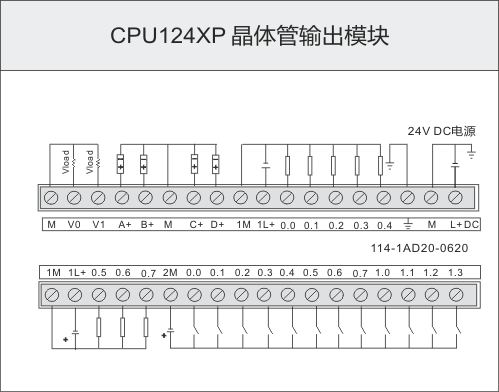
Wiring Diagram:

  
 Definition of Communication Port

|  |  |  |  |
| --- | --- | --- | --- |
| Connector | PIN | FPORT（RS485) | PORT0(RS485) |
| 2014070310362263.jpg | 1 | Shell grounded | Shell grounded |
| 2 | Logical | Logical |
| 3 | RS-485 Signal B | RS-485 Signal B |
| 4 | RTS(TTL) | RTS(TTL) |
| 5 | RTS(TTL) | RTS(TTL) |
| 6 | +5V，100Ω resistances in serie | +5V，100Ω resistances in serie |
| 7 | +24V | +24V |
| 8 | RS-485 Signal A | RS-485 Signal A |
| 9 | / | / |
| Shell | Shell grounded | Shell grounded |

Performance Parameters

|  |  |
| --- | --- |
| Physical Features | |
| Dimension(W×H×D) | 137×80×62mm |
| Power Dissipation | 7W |
| Memory Features | |
| Program Memory | 12KB,8KB for common user，4KB for encrypted users |
| Data Memory | 8KB |
| Power-failure retention power | button batter |
| General Features | |
| Timers in total        1ms        10ms        100ms | 128（T0-T127）  4  16  108 |
| Counters in total | 128(button battery) |
| Internal Memory Bits | 256(button battery) |
| Time interrupts | 2×1ms resolution |
| Edge interrupts | 4 rising edges/ falling edges |
| Boolean execution speed | 0.5µs |
| Float execution speed | 16µs |
| Runtime clock | √ |
| Integrated Communication Features | |
| Communication ports | 2 port，PORT0:PPI， RS485 PWL，  FPROT: free port，RS485 |
| PPI Baud Rate | 9.6, 19.2kbps |
| Baud Rate of Freeport | 1.2k to 115.2kbps |
| Maximum cable length per segment  Isolated repeater applied  Isolated repeater unapplied | When it is 1200 m, 38.4k.  50 m |

|  |  |
| --- | --- |
| Maximum number of stations | 4 stations per segment,32 stations per network |
| Maximum number of masters | 16 |
| Point to point (PPI  Mater Mode) | × |
| Power Function | |
| Input Voltage | 20.4 to 28.8 VDC ， with anti- reverse connection protection |
| +5V power for the extended bus | 500mA |
| 24 VDC Sensor Power supply | × |
| I/O Features | |
| Number of integrated Digital inputs | 12 |
| Input type | Sink/Source |
| Number of integrated Digital outputs | 8 |
| Output type | Solid-MOSFET(Source) |
| Digital I/O mapping area | 128 (64 inputs/64 outputs) |
| Analog I/O mapping area | 32(16 inputs/16 outputs) |
| Maximum number of expandable I/O  modules | 3 |
| Maximum number of digital I/O | 116 |
| Maximum number of  analog I/O | 14AI/8AO |
| Pulse catch inputs | 12 |
| High-speed counters  Single phase counters | 3×50KHz，Support HSC0、HSC1and HSC2 and HSC3，do not support 、HSC4and HSC5,1×30KHZ，support9，HC0 |
| Digital Input Features | |